## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Piurals	Time Stamp
L1	2463	((380/44,265,28) or (377/54,60, 75,67,81) or (340/825.68) or (365/73.78) or (711/109)).CCLS.	USPAT	OR	OFF	2006/03/20 05:26
L2	1467	L1 and register	USPAT	OR	OFF	2006/03/20 05:26
L3.	380	L2 and (permutation (instruction or instructions))	USPAT	OR	OFF	2006/03/20 05:26
L4	57	L2 and (control adj (bit or bits))	USPAT	OR	OFF	2006/03/20 05:26
L5	14	L2 and "bit level"	USPAT	OR	OFF	2006/03/20 05:26
L6		"bit level permutation"	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 05:26
L7	17	L2 and (bit adj2 permutation)	USPAT	OR	OFF	2006/03/20 05:26
L8	7	L2 and ((source and destination) adj2 register)	USPAT	OR	OFF	2006/03/20 05:26
L9	32	L3 and L4	USPAT	OR	OFF	2006/03/20 05:26
L10	32	L3 and L9	USPAT	OR	OFF	2006/03/20 05:26
L11	17	L7 and L3	USPAT	OR	OFF	2006/03/20 05:26
L12	3	L7 and L9	USPAT	OR	OFF	2006/03/20 05:26
L13	475132	(permutation (instruction or instructions))	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 05:26
L14	32	L3 and L4	USPAT	OR	OFF	2006/03/20 05:26
L15	69	(ruby and lee).in.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 05:26
L16	4	L15 and permutation	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 05:26
L17	0	L16 and "defining bit positions in a source register"	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 05:26

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